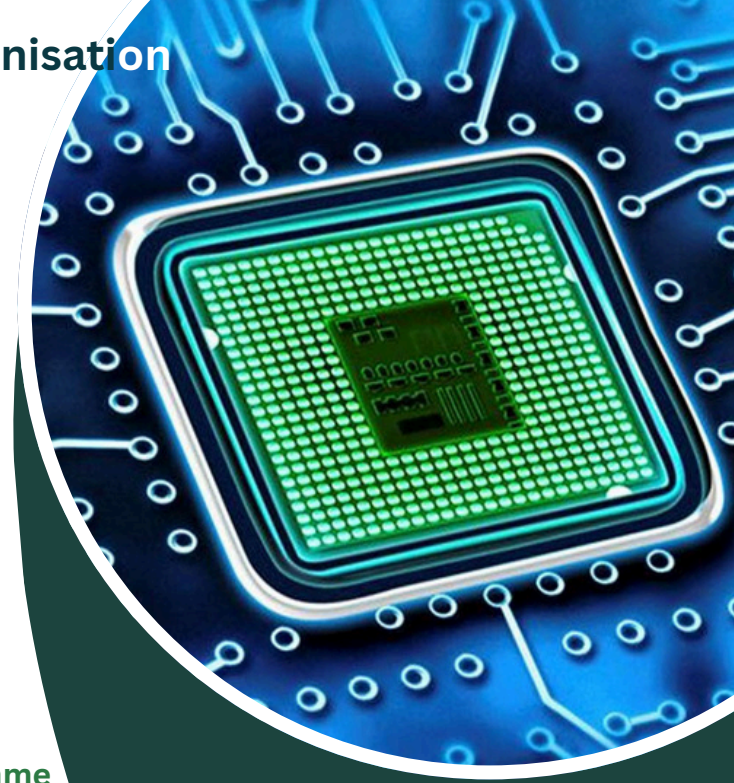
CSIR-Central Scientific Instruments Organisation (CSIR-CSIO), Chandigarh



FUNDAMENTALS OF SEMICONDUCTOR DEVICES

16th to 20th November, 2026

Under CSIR-Integrated Skill Initiative (CSIR-ISI) Programme

in collaboration with

National Institute of Technical Teachers' Training and Research (NITTTR), Chandigarh

Last Date of Registration
15th October, 2026

Limited Seats
30 seats only!
Registration link:
https://forms.gle/FNdmmmqsemB3inVA9

Scan & registration

Highlights of the Workshop

- Knowledge and Practical Exposure on Semiconductor Techniques
- Global and National Overview of Semiconductor Initiatives
- Device fabrication: oxidation, diffusion, and lithography
- Electrical characteristics and performance parameters of devices: Hands-on exposure/demonstrations
- Applications of semiconductor devices in ICs, sensors, and power electronics
- Interaction with experts and discussion on current industry trends
- Convert your idea into industry by getting a grant of Rs.70 Lakh (Rs 20 Lakh for Product Development & Rs. 50 Lakh to start own industry of developed product) under PRISM Scheme of DSIR. The innovator can contact/apply to DSIR-TOCIC, CSIR-CSIO, Chandigarh for further assistance/guidance regarding PRISM Scheme.

Registration Fee

(Including GST)

- Students Only : Rs. 2000/-
- Faculties & Industry : Rs. 3000/-

Selected candidates will get the confirmation Mail and bank details to deposit the Registration fee

Contact Us: Mr. Narinder Singh Jassal, Scientist 'G', nsjassal.csio@csir.res.in, www.csio.res.in