



CSIR-Central Scientific Instruments Organisation (CSIR-CSIO)
Council of Scientific and Industrial Research (CSIR)
Sector 30-C, Chandigarh 160 030
<http://www.csio.res.in>

Advertisement No. 06/2017
[Requirement of Project Staff (in R&D Projects)]

Candidates who fulfill the under-mentioned criteria of age, educational qualifications, and experience etc. may step in for **WALK-IN-INTERVIEW on 3rd August, 2017 (Thursday) at 8:45 a.m. at Auditorium, CSIR-CSIO, Sector-30-C, Chandigarh** along with application form downloadable from our website together with testimonials of all the qualifications (original & attested copies of certificates including SC/ST/OBC/PH). Engagement will be purely on contract/temporary basis co-terminus with the completion of the project. The details of the present available positions are as follows:-

Sr. No.	Name of the Scheme/Project		Positions/ Stipend	No. of Positions	Tenure	Essential Qualification
1.	GAP Project	“Development of an Automated Soil Nutrient Sensing System”	SRF in R&D Project (Rs. 25,000/- p.m. + HRA for 1 st and 2 nd year Rs. 28,000/- p.m. + HRA for 3 rd year) PA Level-I in R&D Project (Rs. 10,000/- p.m. consolidated)	01 SRF & 01 PA Level-I	Upto May, 2020 from the date of engagement (co-terminus with the project duration)	For SRF: Essential Qualification: M.E./M.Tech. in Electronics/ Electrical/ Instrumentation/ Computers or equivalent ⁴ Desired skills: i. Embedded system designing and signal processing, simulations and analysis for electronics development on EDA tools. ii. Experience to develop codes in high level languages such as Python, VC++, etc. iii. Exposure to work on Matlab, R, R-PI, Arduino, etc., hands-on image processing tools and software such as ENVI, etc. iv. Knowledge about soft computing techniques, machine learning and artificial intelligence tools will be an advantage. For PA Level-I: Essential Qualification: Diploma in Electronics/Electrical/Instrumentation /Computers or equivalent ⁴ with at least 55% marks Desirable: i. B.E./ B.Tech. in Electronics/ Electrical/ Instrumentation/ Computers or equivalent ⁴ ii. PCB designing, electronics testion wiring, assembling, circuit simulations, C programming, embedded C programming, microcontrollers, data analysis and processing, etc.
2.	GAP Project	“Design, Development and Supply of HUD Mk-2 units for LCA Airforce and Navy Mk-2 Aircrafts”	PA Level-III in R&D Project (Rs. 28,000/- p.m. + HRA)	10	Upto December, 2018 from the date of engagement or till completion of the project, whichever is earlier	Field: Mechanical (05 positions) B.E./B.Tech. in Mechanical Engineering/Production Engineering or equivalent ⁴ with at least 55% marks plus minimum 2 years of experience in relevant field after acquiring the educational qualification OR M.E./M.Tech. in Mechanical Engineering/Production Engineering or equivalent ⁴ with at least 55% marks

					<p>Desirable:</p> <ul style="list-style-type: none"> i. Knowledge of Mechanical Design/ Design Tools like Solid Works, Autodesk Inventor or Similar/ Operating and Programming of CNC Machines, etc. ii. Knowledge of Imaging Optical Design/Optical Illumination Design/Optics Design Tools like Zemax, OSLO, TracePro or Similar/Optical Assembly, etc. iii. In addition, working knowledge of optical design, simulation and assembly is desired. <p>Field: Electronics (03 positions)</p> <p>B.E./B.Tech. in Electronics/Electronics & Communication Engineering/ Electronics & Instrumentation Engineering/Instrumentation Engineering/Electronics & Control Engineering or equivalent⁴ with at least 55% marks plus minimum 2 years of experience in relevant field after acquiring the educational qualification</p> <p style="text-align: center;">OR</p> <p>M.E./M.Tech. in Electronics/ Electronics & Communication Engineering/ Electronics & Instrumentation Engineering/ Instrumentation Engineering/ Electronics & Control Engineering or equivalent⁴ with at least 55% marks</p> <p>Desirable:</p> <ul style="list-style-type: none"> i. Knowledge of Electronics Design/PCB Design Tools/Reliability and Thermal Analysis Tools/SMD Assembly and Testing/Embedded Systems – ARM/FPGA, etc. ii. In addition, knowledge of optical systems and/or optical thin film coating/simulation is also desired. <p>Field: Computer Science(02 positions)</p> <p>B.E./B.Tech. in Computer Science & Engineering/Software Engineering or equivalent⁴ with at least 55% marks plus minimum 2 years of experience in relevant field after acquiring the educational qualification</p> <p style="text-align: center;">OR</p> <p>M.E./M.Tech. in Computer Science & Engineering/Software Engineering or equivalent⁴ with at least 55% marks</p> <p>Desirable:</p> <ul style="list-style-type: none"> i. Knowledge of Embedded System Design/C Language/C++ Language. ii. Knowledge of optical thin film coating/simulation is also desired. 	
Total vacancies			--	12	--	--

Other Benefits: Accommodation (subject to availability), food facility and other amenities in campus.

Please note:

1. The candidates who have already served CSIR-CSIO or any other lab / institute of CSIR as any Project Assistant / Project Fellow / JRF (in contract R&D projects) / SRF (in contract R&D projects) / Research Associate etc. for a total period of 5 years or more are not eligible for these engagements. The candidates who have served for a period less than 5 years will have tenure upto remaining period till completion of five years.

2. The upper age limit for SRF (in contract R&D project) and Project Assistant Level-III (PA Level-III) is 35 years and for Project Assistant Level-I (PA Level-I) is 28 years.
3. Relaxation in age limit for the candidates belonging to SC / ST / OBC / PH and Women shall be 5 years for the positions of SRF (in contract R&D project), Project Assistant Level-I (PA Level-I) and Project Assistant Level-III (PA Level-III).
4. In case a large No. of candidates turn up for Walk-in-interview, Selection/Screening Committee will have discretion to shortlist the candidates for interview based on written exam or percentage of marks. In respect of equivalence clause in Essential Qualifications, if a candidate is claiming a particular qualification as equivalent qualification for recruitment as per the requirement of advertisement, then the candidate is required to produce order/letter in this regard, indicating the Authority (with number and date) under which it has been so treated otherwise the Application is liable to be rejected.
5. **Medical Facilities** for self will be provided through CSIR-CSIO Dispensary only to the extent available as per rules. This will be limited to self only and not for family members/dependents.
6. **Reservation:** As regards reservation, if all things are equal, SC/ST/OBC/PH candidates may be given preference over General candidates so as to ensure their representation.
7. List of selected and panel of candidates will be displayed on website.
8. The selected candidates are expected to join within fifteen days from the date of receiving of offer of appointment, after completion of pre-appointment formalities like medical examination and character verification etc.
9. The selected candidates will have to deposit an amount equivalent to Fifteen Day's stipend as a Security Deposit (Refundable) with CSIR-CSIO, at the time of joining.
10. CSIR-CSIO reserve the right to cancel or withdraw the award in case of any discrepancy found, in the candidature of any selected candidate at any stage.
11. CSIR-CSIO reserves the right not to fill up a particular position, if it so desires. The number of vacancies indicated above may vary at the time of actual selection.
12. The waitlisted panel will be valid for one year and will be operated within stipulated period as and when required.
13. No TA/DA will be paid to the candidates for attending the interview.
14. The above positions of SRF (in contract R&D project)/PA in CSIR-CSIO are contractual engagement co-terminus with the projects.
15. Other terms & conditions will be governed as per guidelines issued by the funding agency/CSIR Hqrs. for the engagement of above Project Staff as amended from time to time.

Administrative Officer